

DISCHARGE AND SECURITY PULL-UP R'S

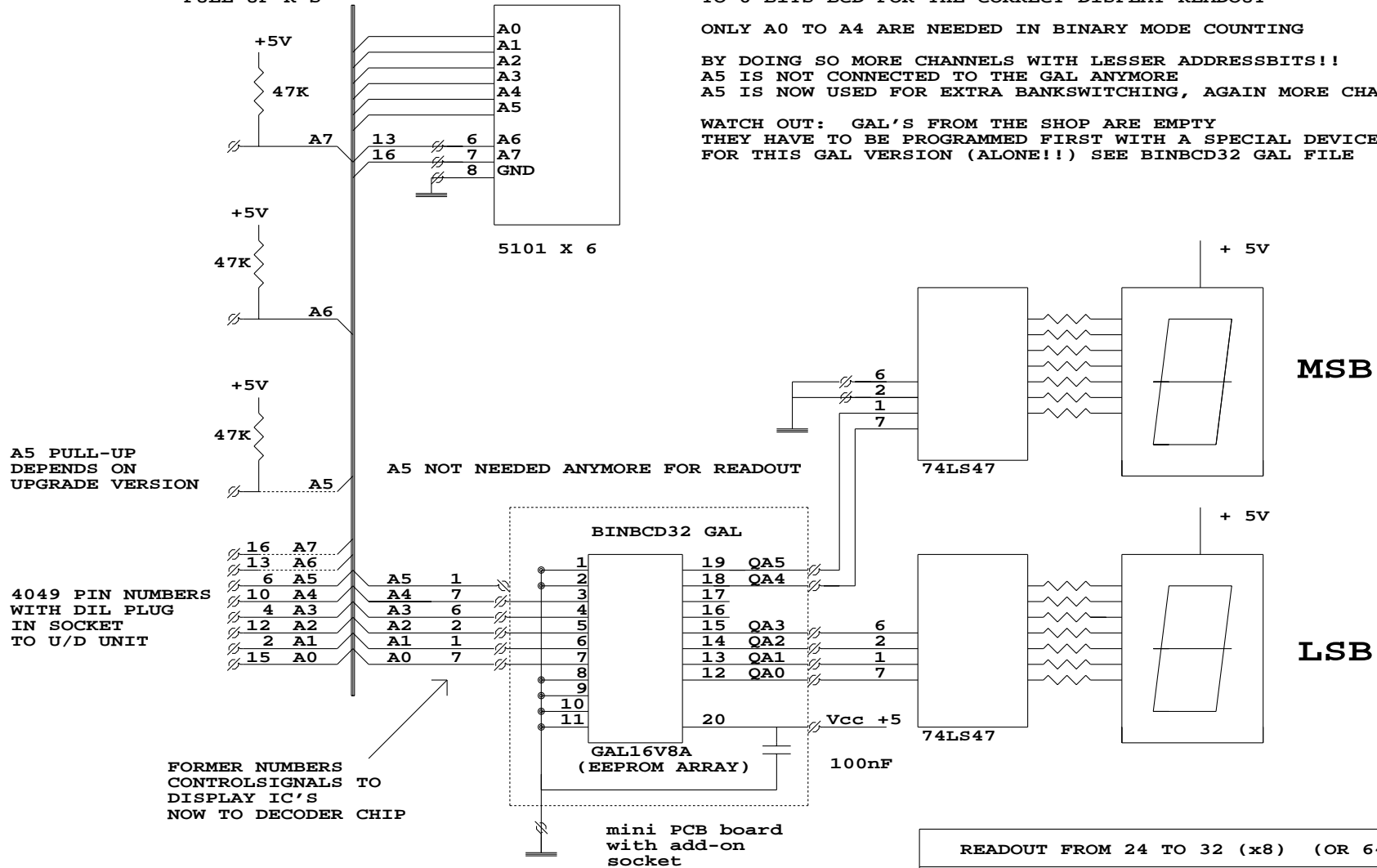
SRAM BANK

THE BINBCD GAL IS PLACED BETWEEN THE 2 74LS47 IC'S IT IS CONNECTED BETWEEN THE CUT INPUT TRACES WITH WIRE-WRAP IT FORMS A DECODER FROM 5 BITS BINARY TO 6 BITS BCD FOR THE CORRECT DISPLAY READOUT

ONLY A0 TO A4 ARE NEEDED IN BINARY MODE COUNTING

BY DOING SO MORE CHANNELS WITH LESSER ADDRESSBITS!! A5 IS NOT CONNECTED TO THE GAL ANYMORE A5 IS NOW USED FOR EXTRA BANKSWITCHING, AGAIN MORE CHANNELS

WATCH OUT: GAL'S FROM THE SHOP ARE EMPTY THEY HAVE TO BE PROGRAMMED FIRST WITH A SPECIAL DEVICE!! FOR THIS GAL VERSION (ALONE!!) SEE BINBCD32 GAL FILE



GAL16V8 ONLY FOR MAX. 32 CHANNEL NUMBERS IF YOU WANT 64 CHANNEL READOUT YOU NEED A GAL22V10 BOARD OR WITH LESS TROUBLE THE 27C64 CMOS EPROM VERSION

READOUT FROM 24 TO 32 (x8) (OR 64 x4)		
Title		
DISPLAY READOUT CORRECTION NDH-518 UPGRADE		
Size	Document Number	REV
A	made by W. Geeraert PELABR	3
Date:	January 4, 2000	Sheet 1 of 1